

Temperature Compensated Crystal Oscillators [TCXO " M " and VCTCXO " VM "]

TCXO			VCTCXO			N series	SMD	Voltage		Min.	Max.
MQN_T	MQN_P	MQN_D	VMQN_T	VMQN_P	VMQN_D			2.5 V	3.3 V	10 MHz	1,500 MHz
CMOS	LVPECL	LVDS	CMOS	LVPECL	LVDS						

Features

0.8 pS Phase Jitter (typical)

The (V)MQN__T, (V)MQN__P and (V)MQN__D Series are members of Mercury's Q-Family Quick-Turn Temperature Compensated oscillators that can be delivered within days. With low current consumption (44 mA for LVPECL 212.500 MHz at 3.3V) and an integrated phase jitter performance of 0.8 pS RMS, they have gained its precision frequency control market position by providing engineers with few-day samples for prototypes and low cost, fast delivery for volume production .

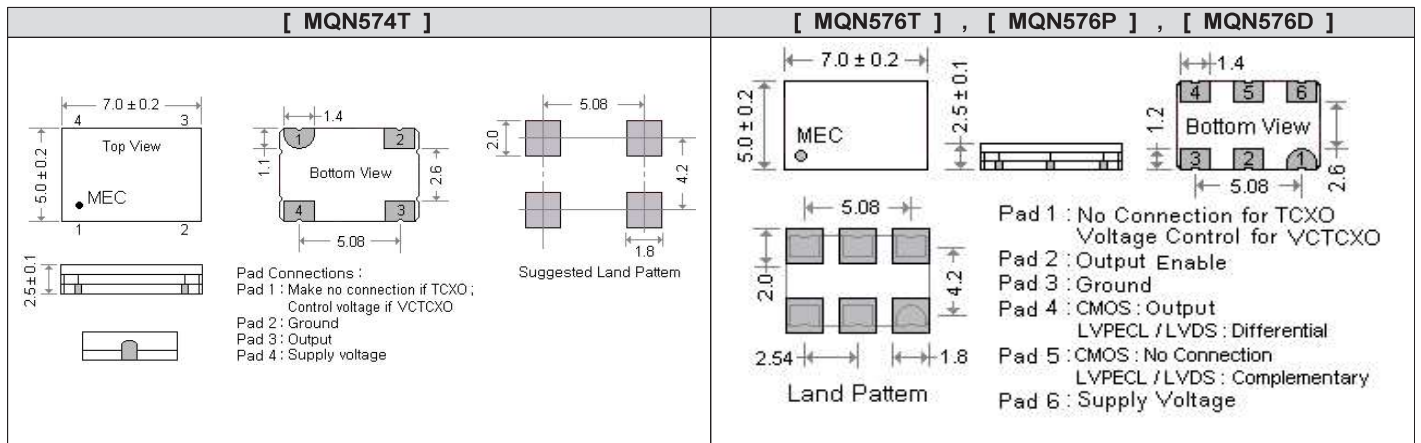


General specifications , at Ta=+25°C , CL=15pF

Model	(V)MQN574T , (V)MQN576T	(V)MQN576P	(V)MQN576D						
Output Logic	LVCMOS / LVTTTL	LVPECL	LVDS						
Supply Voltage V _{DD} (code)	+ 2.5 V _{DD} ± 5% (voltage code " 25 ") + 3.3 V _{DD} ± 5% (voltage code " 33 ")	+ 2.5 V _{DD} ± 5% (voltage code " 25 ") + 3.3 V _{DD} ± 5% (voltage code " 33 ")	+ 2.5 V _{DD} ± 5% (voltage code " 25 ") + 3.3 V _{DD} ± 5% (voltage code " 33 ")						
Available Frequency Range	10 ~ 250 MHz	10 ~ 1,500 MHz	10 ~ 1,500 MHz						
Load	15 pF	50 Ω into Vcc - 2V or Thevenin equivalent	100 Ω						
Output Logic " High " , " 1 "	90 % V _{DD}	V _{DD} - 1.03 (min.) , V _{DD} - 0.6 (max.)	1.4 V Typical , 1.6 V max.						
Output Logic " Low " , " 0 "	10 % V _{DD}	V _{DD} - 1.85 (min.) , V _{DD} - 1.6 (max.)	1.1 V Typical , 0.9 V min.						
(V _{DD} = + 2.5V)									
Current Consumption	50 MHz : 24 mA 125 MHz : 28 mA 200 MHz : 30 mA	156 MHz : 36 mA 600 MHz : 40 mA 800 MHz : 46 mA 1,000 MHz : 50 mA	156 MHz : 22 mA 600 MHz : 28 mA 800 MHz : 30 mA 1,000 MHz : 34 mA						
All values are typical and over the operating temperatures.									
(V _{DD} = + 3.3V)									
Current Consumption	50 MHz : 26 mA 125 MHz : 30mA 200 MHz : 34 mA	156 MHz : 40 mA 600 MHz : 45 mA 800 MHz : 48 mA 1,000 MHz : 52 mA	156 MHz : 25 mA 600 MHz : 30 mA 800 MHz : 32 mA 1,000 MHz : 36 mA						
All values are typical and over the operating temperatures.									
Current with Output Disabled	18 mA (typical)	18 mA (typical)	18 mA (typical)						
Rise Time / Fall Time	1.5 nS. (Typical) , 3.0 nS. (max.) Tr / Tf : 10% ↔ 90% waveform	0.2 nS. (Typical) , 0.5 nS. (max.) Tr / Tf : 20% ↔ 80% waveform	0.2 nS. (Typical) , 0.4 nS. (max.) Tr / Tf : 20% ↔ 80% waveform						
Initial Calibration Tolerance	±2.0 ppm. max. at +25°C±2°C. (upon shipment)								
Frequency Stability Codes	Temperature (ref to +25°C)	± 2.5 ppm over -30°C to +85°C (default) ± 1.0 ppm over -40°C to +85°C (available)							
	Aging	± 1.0 ppm max . , per year at 25°C							
	Voltage Change	± 0.2 ppm max . , for a ±5% input voltage change.							
	Load Change	± 0.2 ppm max . , for a ±10% load condition change.							
	Reflow	± 1.0 ppm max . , 1 reflow and measured 24 hours afterwards.							
Duty Cycle	50 % ± 5%								
Start-up Time	5 m sec. (max.)								
Aging at Ta = +25°C	± 2 ppm max. first year at 25°C ; ± 10 ppm max. over 10 years								
Storage Temperature	-55°C to + 150°C								
SSB Phase Noise [dBc / Hz (typical)]	Offset	10 Hz	100 Hz	1K Hz	10K Hz	100K Hz	1M Hz	10M Hz	Phase Jitter (12KHz ~ 20 MHz)
	125 MHz	-51	-93	-111	-123	-125	-135	-155	0.73 pS
	212.5 MHz	-42	-87	-105	-115	-118	-130	-151	0.85 pS
312.5 MHz	-49	-88	-107	-111	-114	-124	-147	0.88 pS	

Control Voltage Function on Pad 1		Output Enable Function on pad 2	
Control Voltage Center and Range	+1.5V ± 1.0V for both V _{DD} = 2.5V and 3.3V	OE Control on Pad 2	70% of V _{DD} (min.) to enable output. (Open connection prohibit.)
Frequency Pulling Range	± 8 ppm min.		30% of V _{DD} (max.) to disable output (high impedance).
Linearity	± 1 % typical ± 10% max.	Output Enable Time / Disable Time	200 nS. Max. / 50 nS. Max.
Transfer Function	Positive Transfer	Integrated Phase Jitter	0.8 pS typical (12 KHz to 20 MHz) < 150 fS (1.875 KHz to 20 MHz)
Absolute Voltage	4.0 V max.		
Input Impedance	770 KΩ typical.		
Harmonics	-5.0 dBc max.		

Outline Dimensions (Unit : mm) , Suggested pad Layout for SMDs



Mercury www.mercury-crystal.com

■ Taiwan : Tel:(+886)-2-2406-2779 / sales-tw@mercury-crystal.com

■ USA: Tel: (+1)-909-466-0427 / sales-us@mercury-crystal.com

■ China: Tel: (+86)-512-5763-8100 / sales-cn@mercury-crystal.com

Temperature Compensated Crystal Oscillators [TCXO " M " and VCTCXO " VM "]

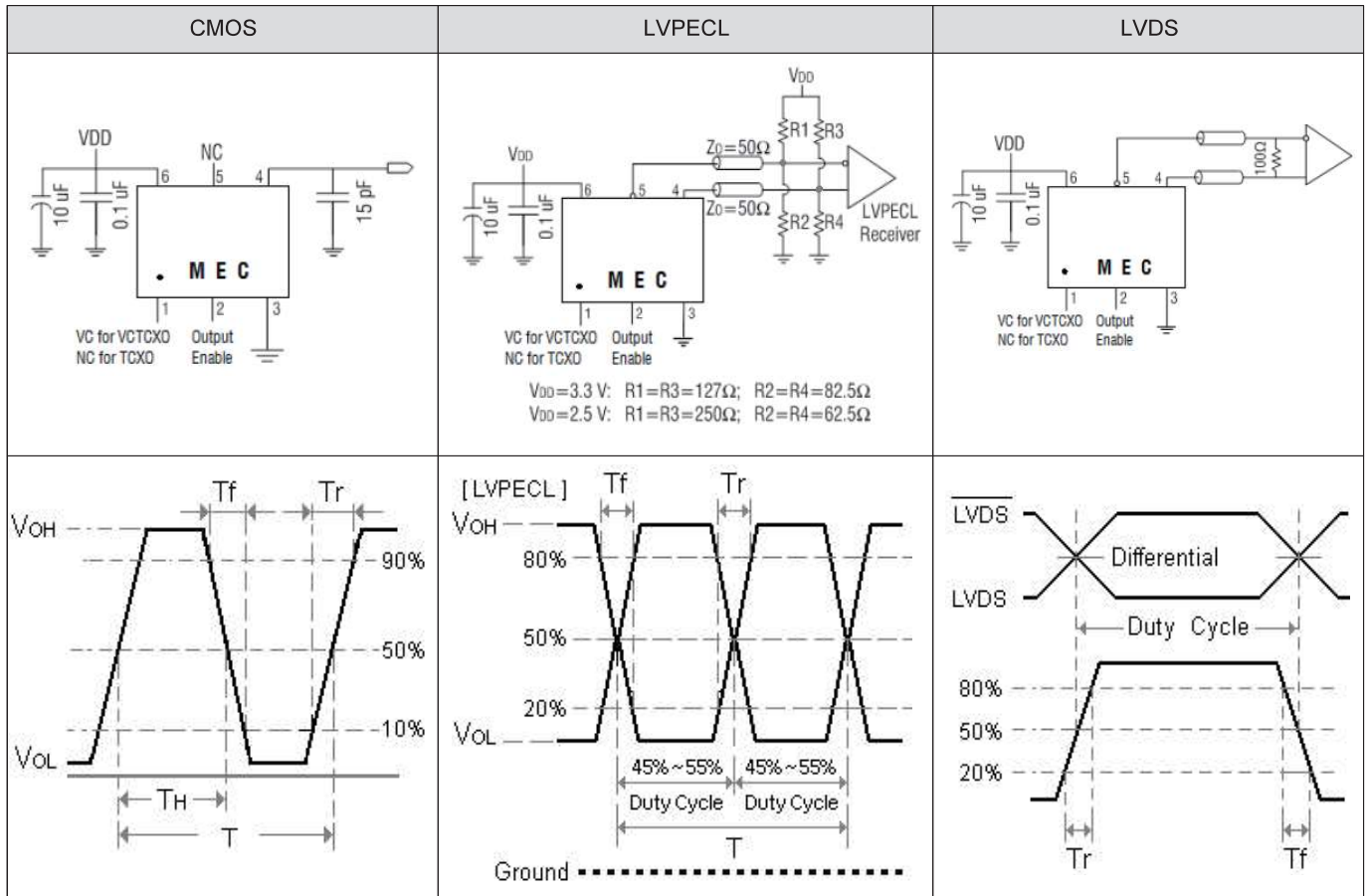
TCXO			VCTCXO			N series	SMD	2.5 V	3.3 V	Min. 10 MHz	Max. 1,500 MHz
MQN_T	MQN_P	MQN_D	VMQN_T	VMQN_P	VMQN_D						

Part Number Format and Example

- Example :
- 1.VMQN574T33 - 120.000 - 2.5 / -40+85
 - 2.VMQN576P33 - 120.000 - 2.5 / -40+85

VMQN	574	T	33	-	120.000	-	2.5	/	-40+85
Hold Type	Package	T : CMOS	Supply Voltage		Center Freq. (MHz)		Freq. Stability		Operating Temperature Range
" MQN " : TCXO	" 574 " 4pad (5.0 * 7.0 mm)		" 33 " for 3.3V " 25 " for 2.5V						
" VMQN " : VCTCXO									
VMQN	576	P	33	-	120.000	-	2.5	/	-40+85
Hold Type	Package	T : CMOS	Supply Voltage		Center Freq. (MHz)		Freq. Stability		Operating Temperature Range
" MQN " : TCXO	" 576 " 6pad (5.0 * 7.0 mm)	P : LVPECL D : LVDS	" 33 " for 3.3V " 25 " for 2.5V						
" VMQN " : VCTCXO									

Test Circuits and Output Waveforms



Output OE Function on pad 2



TCXOs