

HDK

LVDS Differential (Non - PLL)

Jitter **0.2 ps** (typical)

SMD

1.8 V

2.5 V

3.3 V

Min.

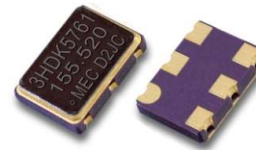
13.5 MHz

Max.

200 MHz

Features

- Femto second integrated phase jitter (200 fs typical , 12 KHz to 20 MHz)
- Superior phase noise (-138 dBc/Hz at 10 KHz and -144 dBc/Hz at 100 KHz offset)



General specifications , at Ta=+25°C

Output Logic		LVDS Differential				
Model		HDK				
Package (dimensions) unit : mm		HDK 3261 (3.2 * 2.5 * 1.0)	HDK 5361 (5.0 * 3.2 * 1.2)	HDK 5761 (7.0 * 5.0 * 1.8)		
Supply Voltage V _{DD}		+1.8 V _{DD} ± 5%	+2.5 V _{DD} ± 5%	+3.3 V _{DD} ± 10%		
Supply Voltage Code		" 18 "	" 25 "	" 3 "		
Available	min.	13.5 MHz				
Frequency Range	max.	200.0 MHz				
Rise Time / Fall Time (20% □ 80% of wave form)		0.3 nS typical 0.6 nS max.	0.15 nS typical 0.4 nS max.	0.15 nS typical 0.4 nS max.		
Current Consumption		16 mA typical , 27 mA max.				
Integrated Phase Jitter (12 KHz to 20 MHz)		0.2 ps typical; 0.5 ps max. [For 156.250 MHz , 3.3V]				
SSB Phase Noise [dBc / Hz (typical)]		Offset	62.5 MHz (3.3V)	156.250 MHz (3.3V)		
		10 Hz	-50	-50		
		100 Hz	-82	-80		
		1 KHz	-116	-115		
		10 KHz	-138	-135		
		100 KHz	-144	-142		
		1 MHz	-149	-147		
Output Logic " High " , " 1 "		1.4 V (typical) ; 1.6 V (max.) , RL = 100 Ω ,				
Output Logic " Low " , " 0 "		0.9 V (min.) ; 1.1 V (typical) , RL = 100 Ω ,				
Output Voltage Swing		250 mV min. , 350 mV typ. , 450 mV max. , RL = 100 Ω ,				
Load		100 Ω between output and complimentary output				
Start-up Time		5.0 ms typical , 10 m sec. (max.)				
Duty Cycle		50% ± 5%				
Storage Temperature		-55°C to + 125°C				
Aging at Ta = +25°C		± 3 ppm max. first year ; ± 2 ppm max. per year thereafter				
Frequency Stability Codes		Frequency Stability over Operating Temperature Range	± 25 ppm	± 50 ppm	± 100 ppm	If non-standard, please enter the desired stability after the " C " or " I " represents . For example : " C20 " ± 20 ppm over -10°C to +70°C ; " I30 " ± 30 ppm over -40°C to +85°C
		Commercial (-10°C to +70°C)	A	B	C	
		Industrial (-40°C to +85°C)	D	E	F	
OE Function. 5761 on pad 1		Enable	When 70% min. of V _{DD} to Enable Output. Enable time : 10 ms max.			
		Disable	When 30% max. of V _{DD} to Disable Output. Disable current : 10 uA max. , Disable time : 0.2 us (max.)			

Outline Dimensions (Unit : mm) , Suggested pad Layout for SMDs

HDK3261	HDK5361	HDK5761
<p>Pad 1 : OE Pad 2 : No Connection Pad 3 : Ground Pad 4 : Output Pad 5 : Complementary Pad 6 : Supply Voltage</p>	<p>Pad 1 : OE Pad 2 : No Connection Pad 3 : Ground Pad 4 : Output Pad 5 : Complementary Pad 6 : Supply Voltage</p>	<p>pad 1 : OE pad 2 : No connection pad 3 : Ground pad 4 : Output pad 5 : Complementary pad 6 : Supply Voltage</p>

Mercury www.mercury-crystal.com

■Taiwan : Tel (886)-2-2406-2779 / sales-tw@mercury-crystal.com ■U.S.A: Tel: (1)-909-466-0427 / sales-us@mercury-crystal.com ■China: Tel: (86)-512-5763-8100 / sales-cn@mecxtal.com